



SYSTEMS CONCEPTS

520 THIRD STREET SAN FRANCISCO, CALIFORNIA 94107

December 8, 1977

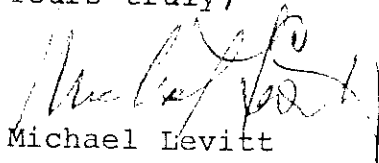
Mr. T. E. Killebrew, Director
Procurement Services Department
Stanford University
851 Welch Road
Palo Alto, California 94304

Dear Mr. Killebrew:

Enclosed for your approval is a copy of the Acceptance Plan for the Digital Synthesizer, as discussed by us and Prof. Chowning. Various minor modifications have been incorporated, including those at your request regarding Messrs. Moorer, Panofsky, and Rubin. A copy has been transmitted to Prof. Chowning.

We trust this plan will meet with your agreement, but if there should be any question please do not hesitate to call.

Yours truly,


Michael Levitt

ACCEPTANCE PLAN, Systems Concepts Digital Synthesizer
(Purchase Order No. C601535, Change Order No. 2)

1. Operation

Until Stanford University (SU) accepts the Synthesizer, its operation will be carefully restricted by SU. It may be operated only for testing it against the specifications included, by reference, in the purchase order and only by means of its computer interface, power switch, and connections to its four analog outputs. Its power switch will not be operated unnecessarily; and other precautions, described under "TAKE CARE" in the theory-of-operation manual, will be observed.

2. Acceptance

Tests of the Synthesizer bearing on its acceptance are to be run by SU. If no hardware failures occur during operation of the Synthesizer on or before January 31, 1978, then SU will notify Systems Concepts (SC) of its acceptance or rejection of the Synthesizer by 5:00 pm on that day. Otherwise, the deadline for SU's acceptance or rejection will be delayed the same amount of time that SC, having been notified of a failure, uses to treat the failure if it is a hardware failure. A hardware failure is a failure by the Synthesizer to perform in accordance with the aforementioned specifications. If a failure occurs, SC may examine the program being run at the time, to determine whether the failure is due to SC hardware.

3. Documentation

Complete but preliminary documentation of the Synthesizer hardware, as listed below, will be delivered by SC immediately; however, its use will be carefully restricted by SU. Until the end of the hardware warranty period, SU will not make the documentation available to anyone but J. Chowning, K. Shoemake, G. Loy, and A. Moorner, with the following exception. During the last ten days of the hardware warranty period, while T. Panofsky and J. Rubin are being trained in maintenance of the Synthesizer, SU may make the documentation available to them, for that purpose only. During the acceptance and hardware warranty periods, SC will inform the principal investigator of any documentation changes resulting from hardware updates. At the end of the hardware warranty period, SC will provide complete and final versions of the documentation listed below, a final version of the programming specification, and a user's manual for the SC Synthesizer diagnostic programs.

- 1) Digital Synthesizer Theory of Operation
Manual
- 2) Power Sequencer Theory of Operation Manual
- 3) Engineering Drawing Conventions
- 4) Digital Synthesizer Engineering Drawings,
Volume 1
- 5) Digital Synthesizer Engineering Drawings,
Volume 2

The final versions of the engineering drawings will be in 11x17 format.

4. Warranties

Both warranty periods will begin on January 1, 1978. If the amount of time used by SC to treat hardware failures before the Synthesizer is accepted is in excess of 14 days, then both warranty periods will be extended by the excess.

5. Payment

SU will make payment in full for the Synthesizer within seven calendar days following its acceptance.

Agreed to:

Systems Concepts, Inc.

by

.....*Michael Port*.....

(Signature)

.....President.....

(Title)

.....December 9, 1977.....

(Date)

Stanford University

by

.....*JE Kildrew*.....

(Signature)

.....*Director of Proc. Svcs.*.....

(Title)

.....*12/9/77*.....

(Date)