(Most of the day was taken in discussion and efforts to come to agreement. The notes below are primarily things taken down from the white-board.)

Research topics   Name of person to write section for inclusion
                                         in proposal (copies to be sent to JC,FRM,BV)

1. System Design
   A. Interconnection   (PG) ✓
   B. Integration   (DGL)
   C. Debugging   (TVR) ✓
   D. Storage   (PG)

2. Composer Environment
   A. Performer Interface   (MUZ,FRM,BV)
   B. Composition Langauge   (BIL)
   C. Editors   (MUZ)
   D. Extensible Representation   (DFP)
3. Performance Interface
   A. Control devices   (FRM)
         (Hardware/Software)
   B. Output Display   (PG) ✓
   C. Mapping of Control Inputs (MSP)
   D. Scheduling   (TVR) ✓
         (RT multiprocessing)
         (See also #3F)
   E. Interpretdtion   (BV?)
   F. Multitasking guided by (MUZ)
         music intelligence

4. Signal Processing
   A. DAC-ADC   (---)
   B. Chip design   (MSP)
   C. Board-level design   (FRM)
   D. Control   (TVR) ✓
         (Low level control)
         (Intermediate level
            formerly #4G)
   E. Microcode Compiler   (PG) ✓
   F. Algorithms   (JOS) ✓
         (Developement/
               implementation)

For proposal – Describe above research topics as follows:

   Task area (in COMITS)
   Research needs (what is needed for implementation)
     People
     Hardware
     Software
     Time
     Dependencies on other tasks, when strong
   Should be 2-3 pages
   Include appendix if more detailed explanations are needed

   Complete by Friday, 18 February – (copies to JC,FRM,David Place for BV)