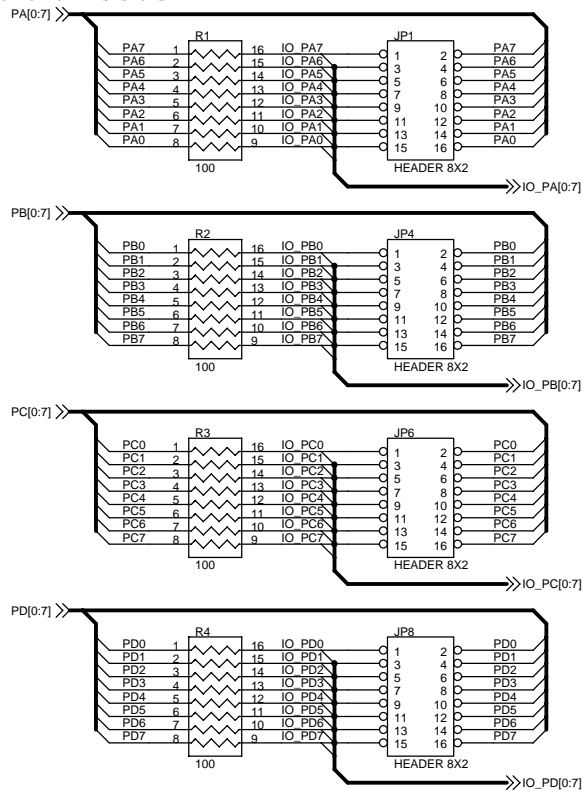
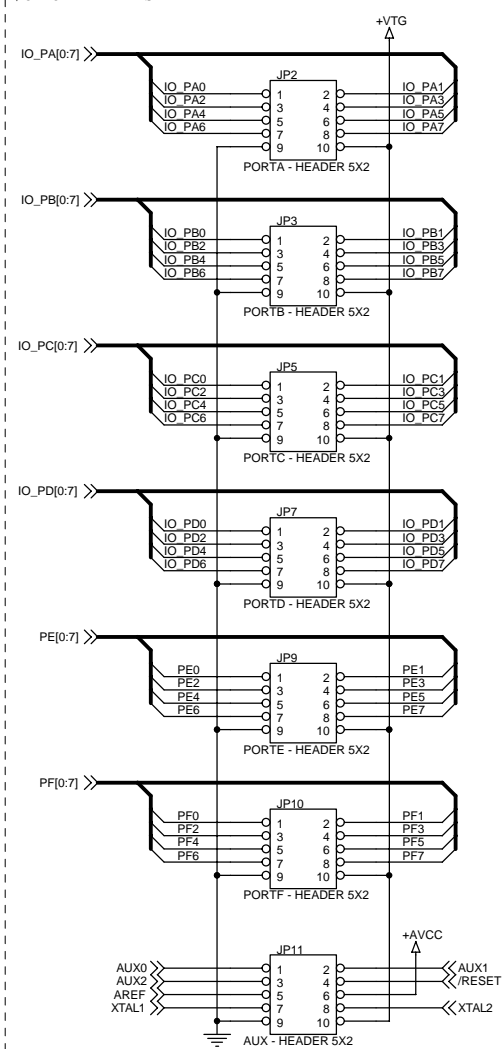


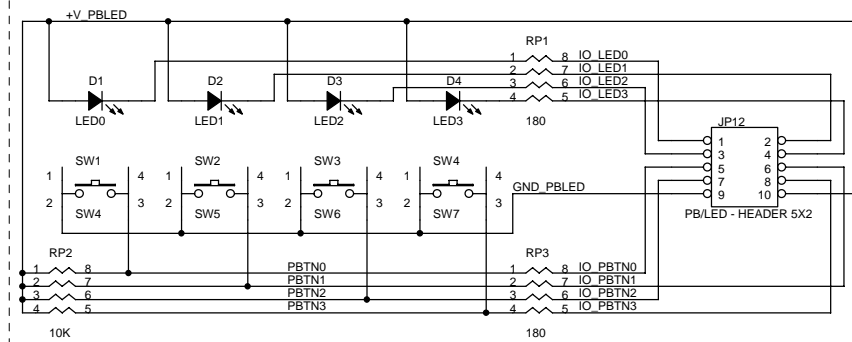
I/O PORT PROTECTION RESISTORS



I/O PORT HEADERS



PUSHBUTTONS AND LEDs

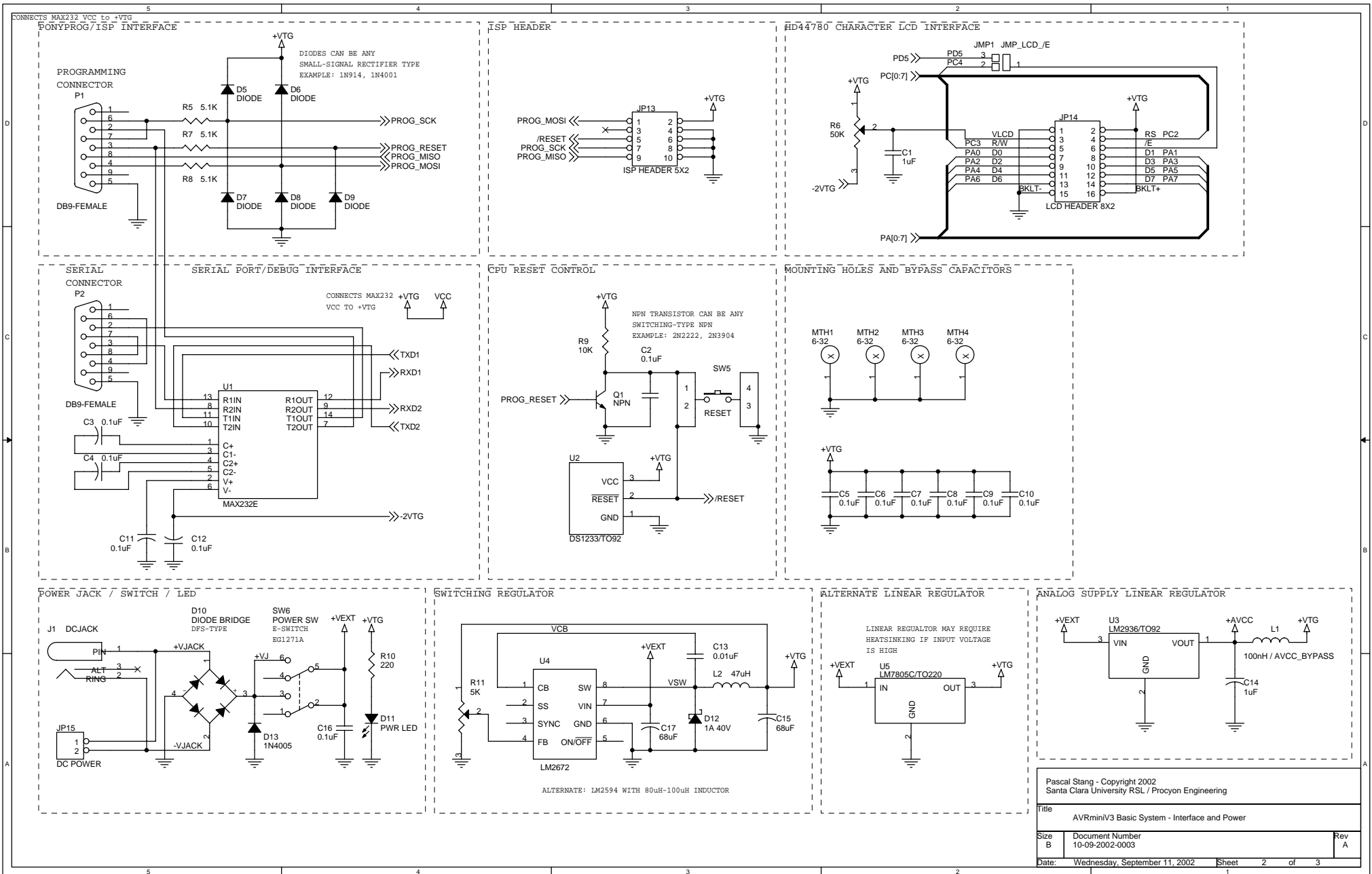


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Title: AVRminiV3 Basic System - I/O Ports

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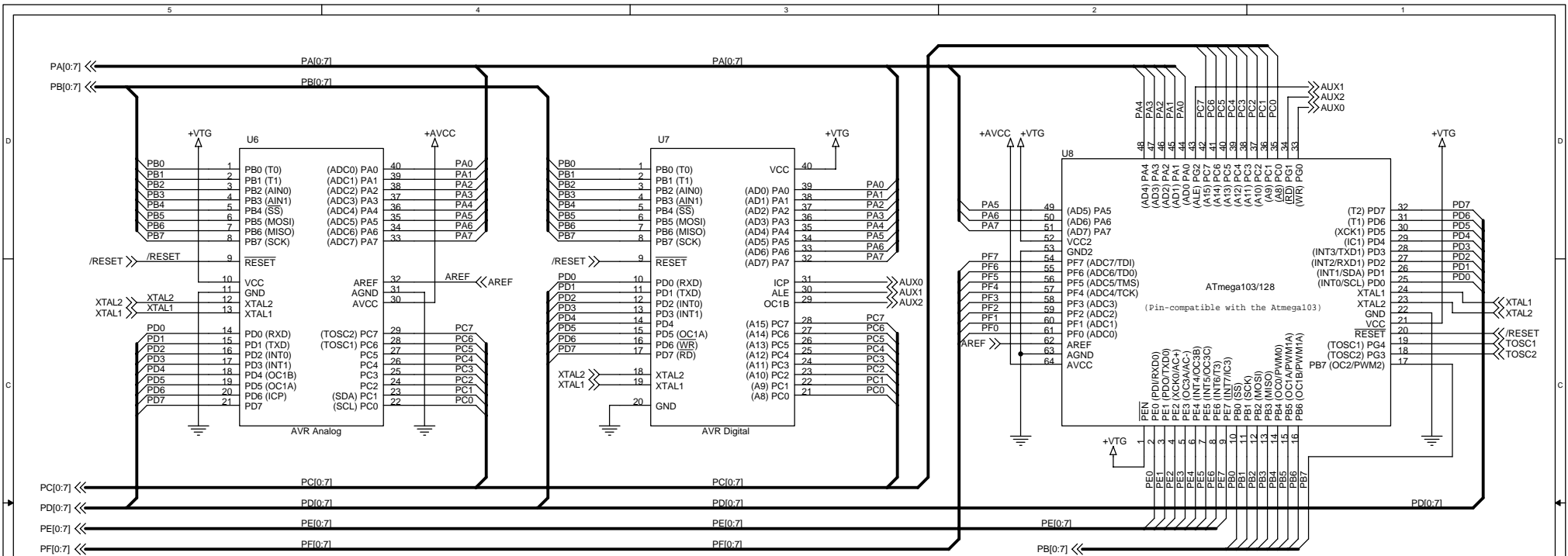


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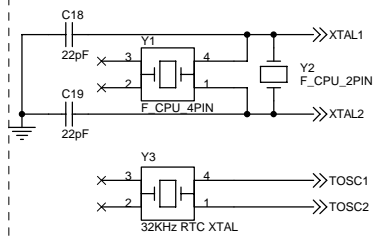
Title: AVRminiV3 Basic System - Interface and Power

Size B	Document Number 10-09-2002-0003	Rev A
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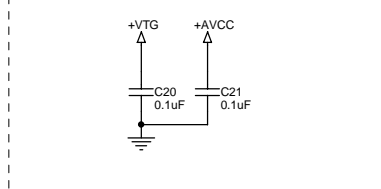
Date: Wednesday, September 11, 2002 Sheet 2 of 3



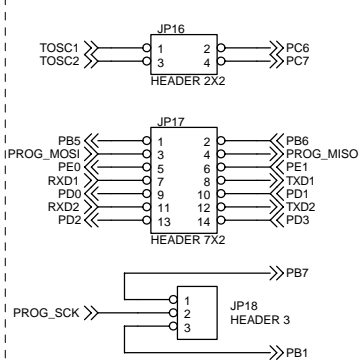
MAIN CLOCK AND RTC CRYSTALS



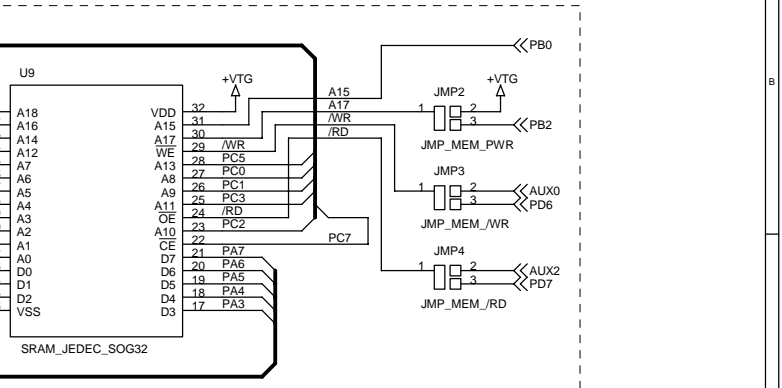
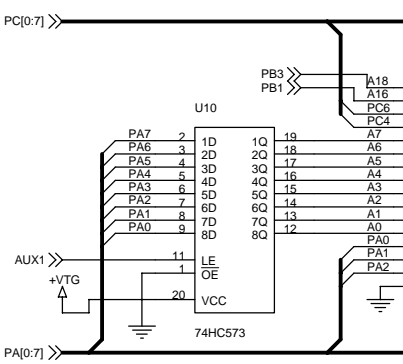
CPU BYPASS CAPACITORS



CONFIG JUMPERS



EXTERNAL SRAM MEMORY



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Title		
AVRminiV3 Basic System - Processor and Memory		
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